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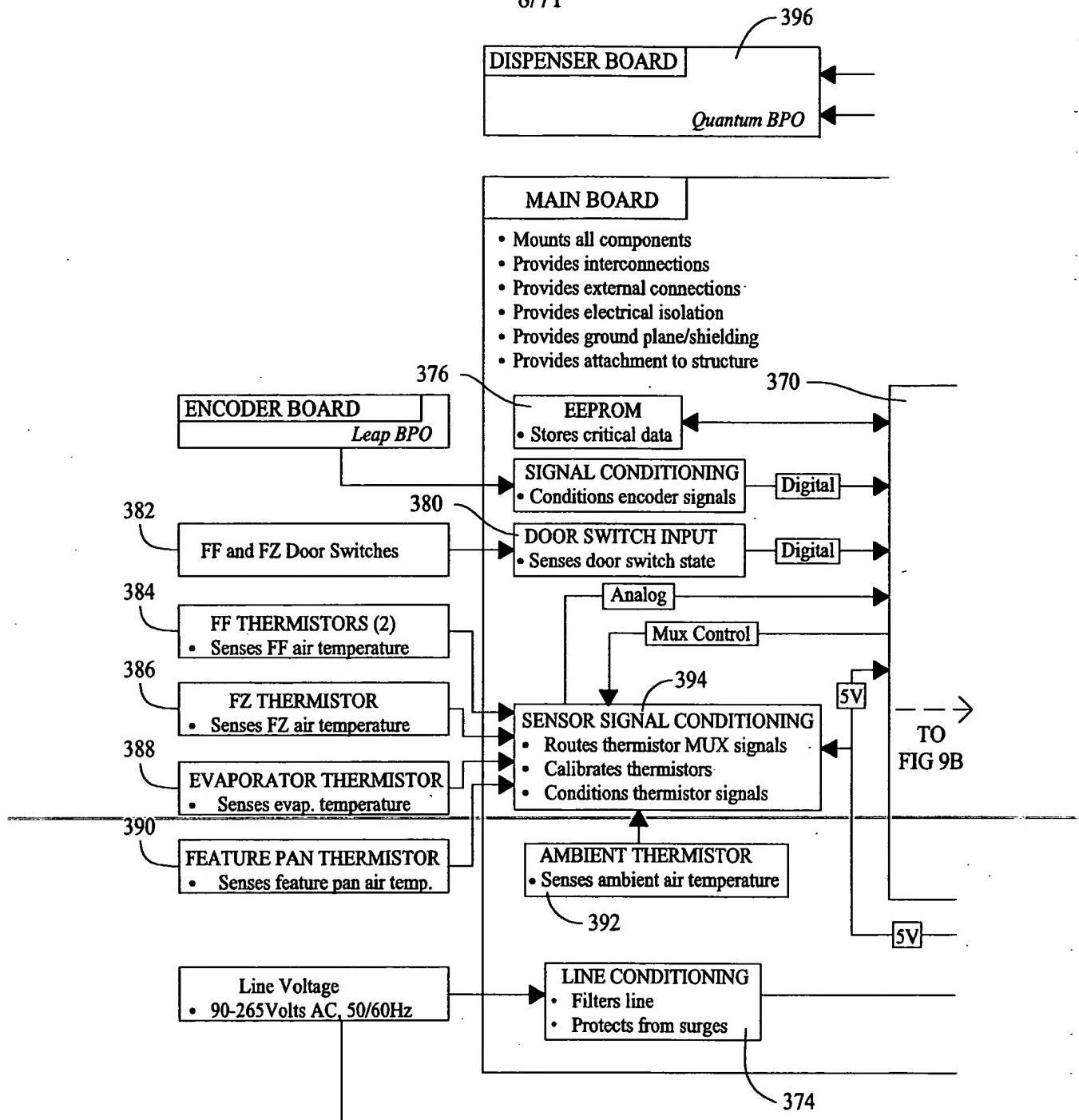
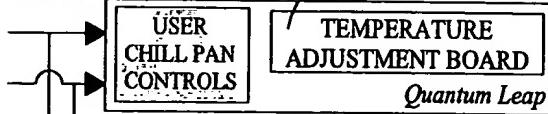


FIG. 9A

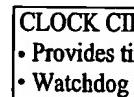
← TO
FIG 9A

398



326

400



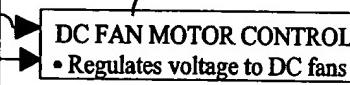
378

LOGIC

- Temperature Adjust/Dispenser Comm.
 - Receives/Decodes bits
 - Executes decoded data
 - Encodes messages
 - Sends bits to other modules
- AC Device Control
 - Turns on/off relays
- Signal Conditioning
 - Converts analog signal to digital
 - Controls mux
 - Processes/Filters digital signal
- Runs Algorithms
 - Sealed system control
 - Evaporator fan control
 - Defrost control
 - Feature pan control
 - FF fan control
 - Stepper motor damper control
 - Auger motor control
 - Cube/Crush solenoid control
 - Timer control
 - Self-test
- Microprocessor Hardware Watchdog
 - Configures/Resets watchdog
- Reads/Writes Critical Data to EEPROM

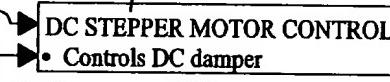
12V

402



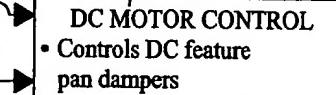
12V

404



12V

406

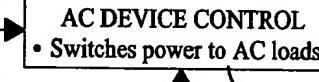


12V

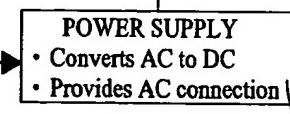
408



410



372



326

368

364

366

274

360

260

266

350

344

412

346

414

356

Evaporator Fan

Condenser Fan

FF Fan

Feature Pan Fan

Mullion Damper

Feature Pan Single Damper

Feature Pan Dual Damper

Water valves (2)

Cube/Crush Solenoid

Compressor

Auger Motor

Feature Pan Heater

Defrost Heater

FIG. 9B

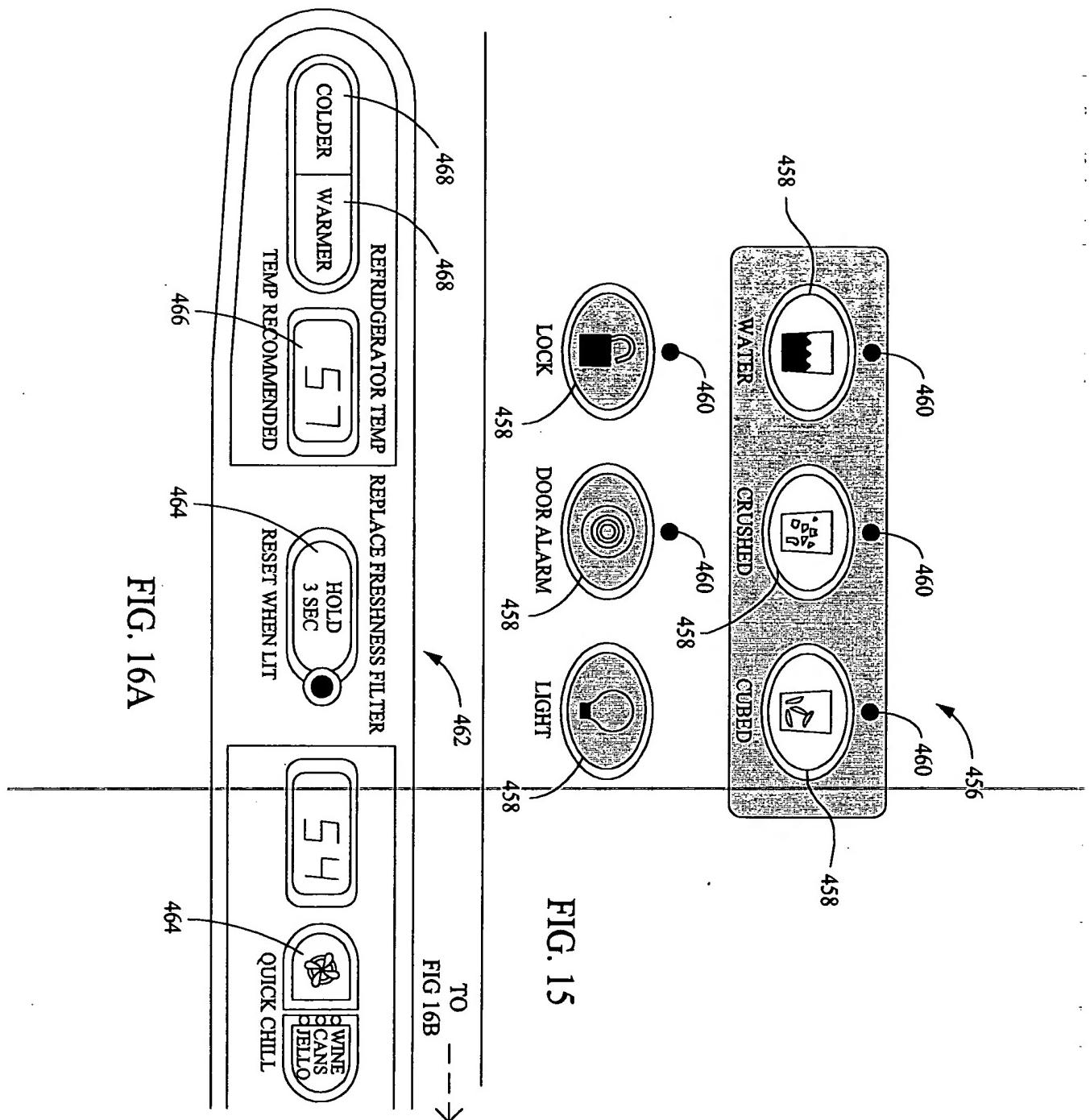


FIG. 16A

FIG. 15

16/71

← TO
FIG 16A

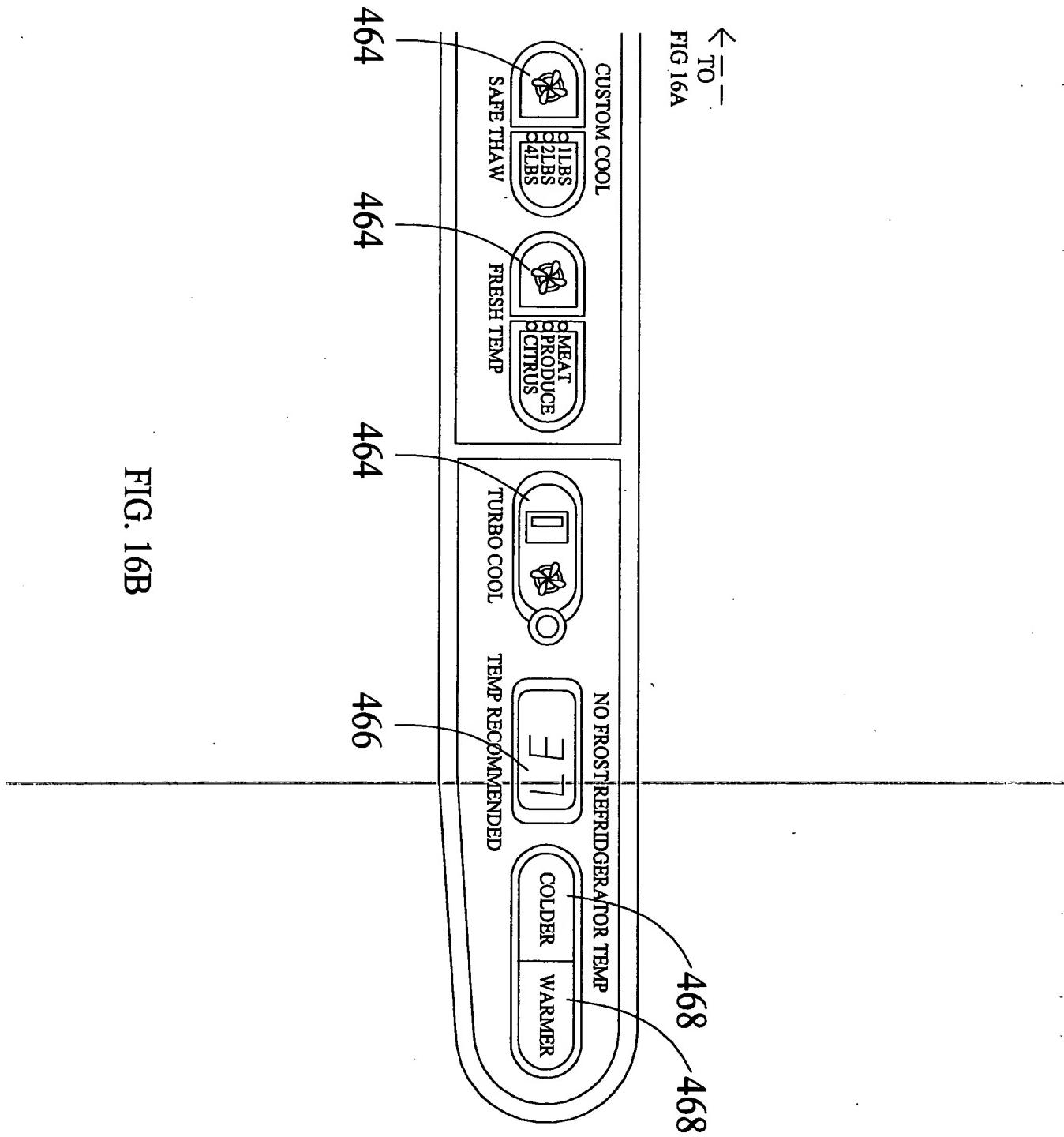
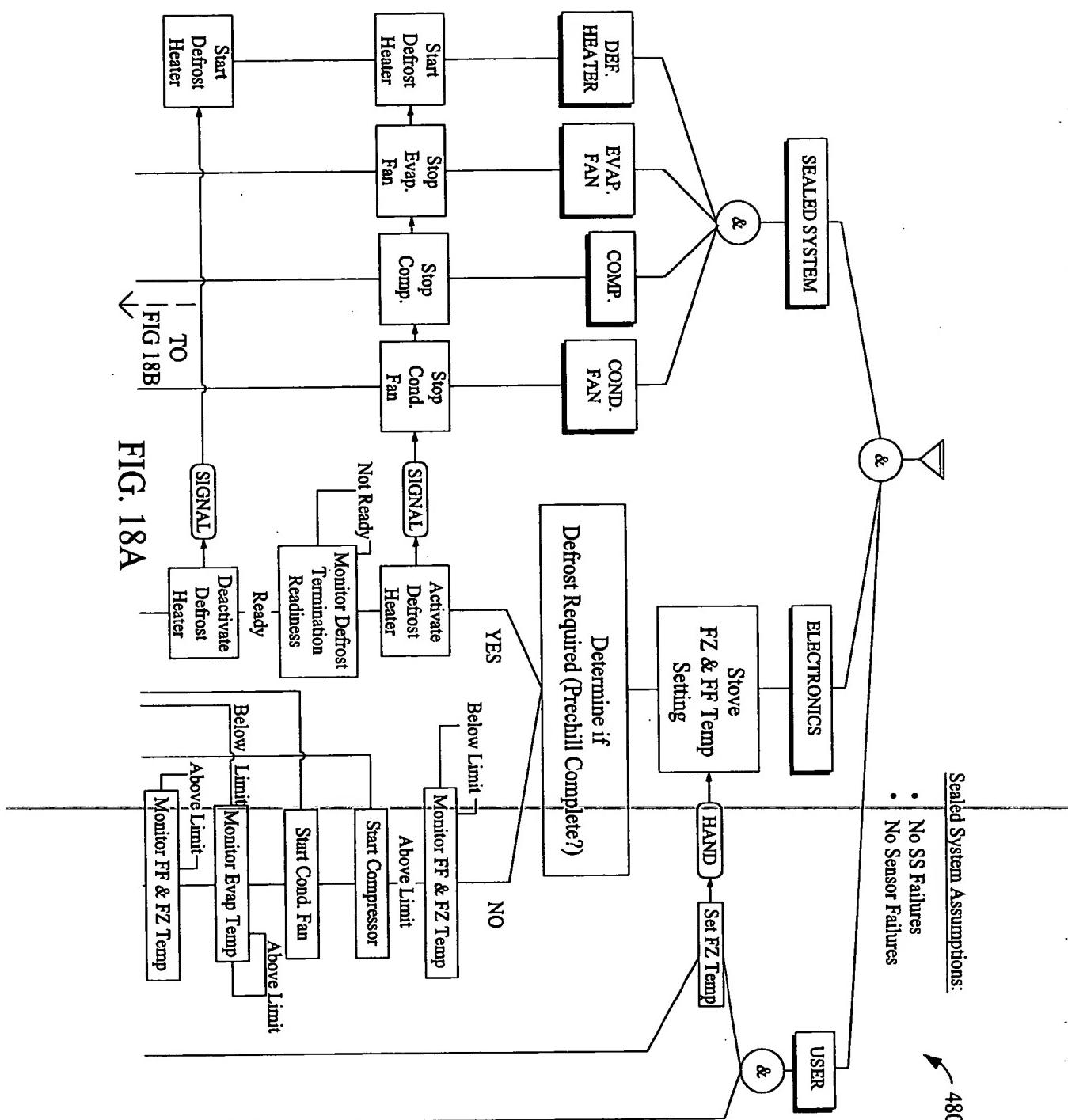
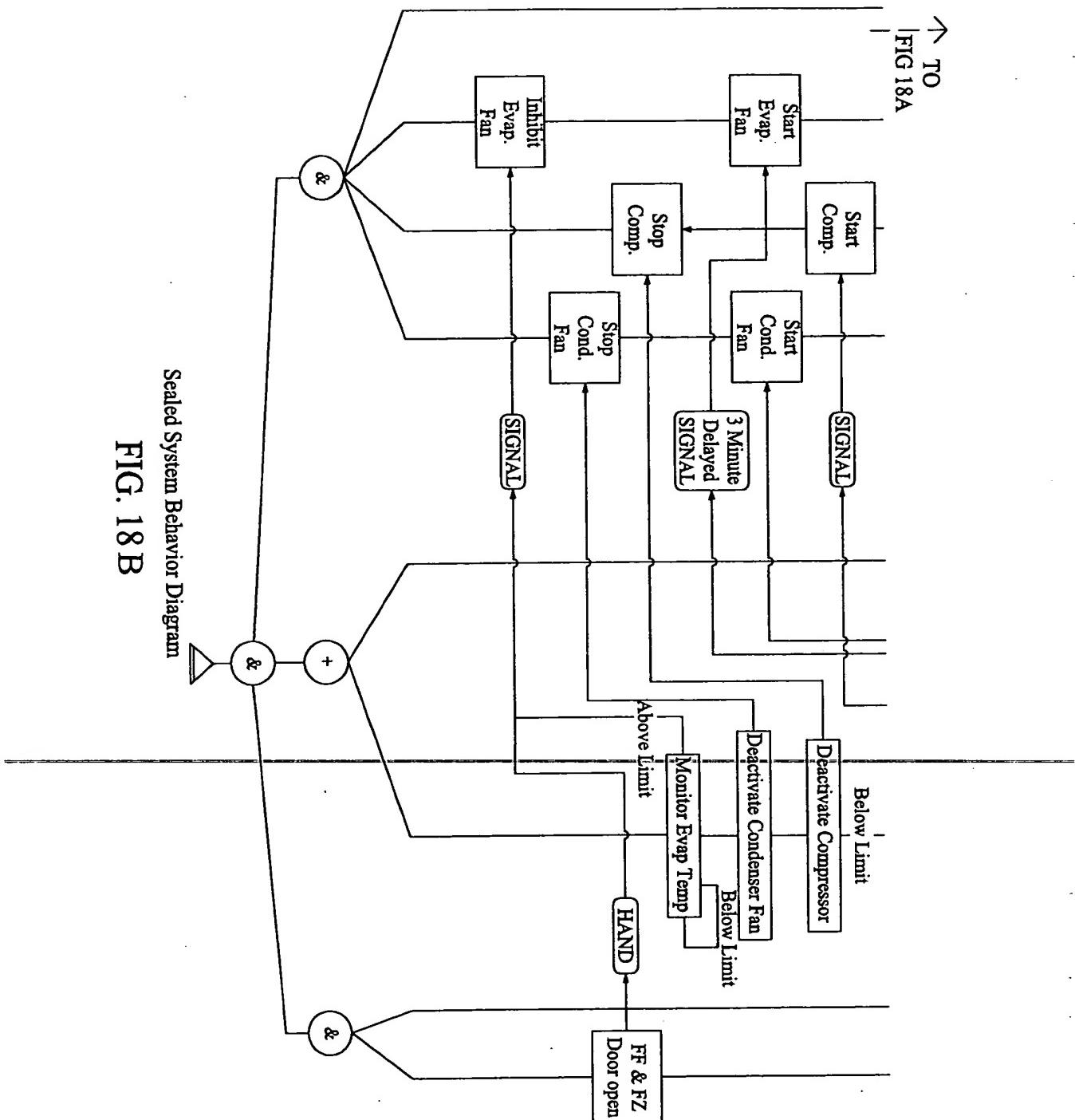


FIG. 16B

18/71



↑ TO
FIG 18A



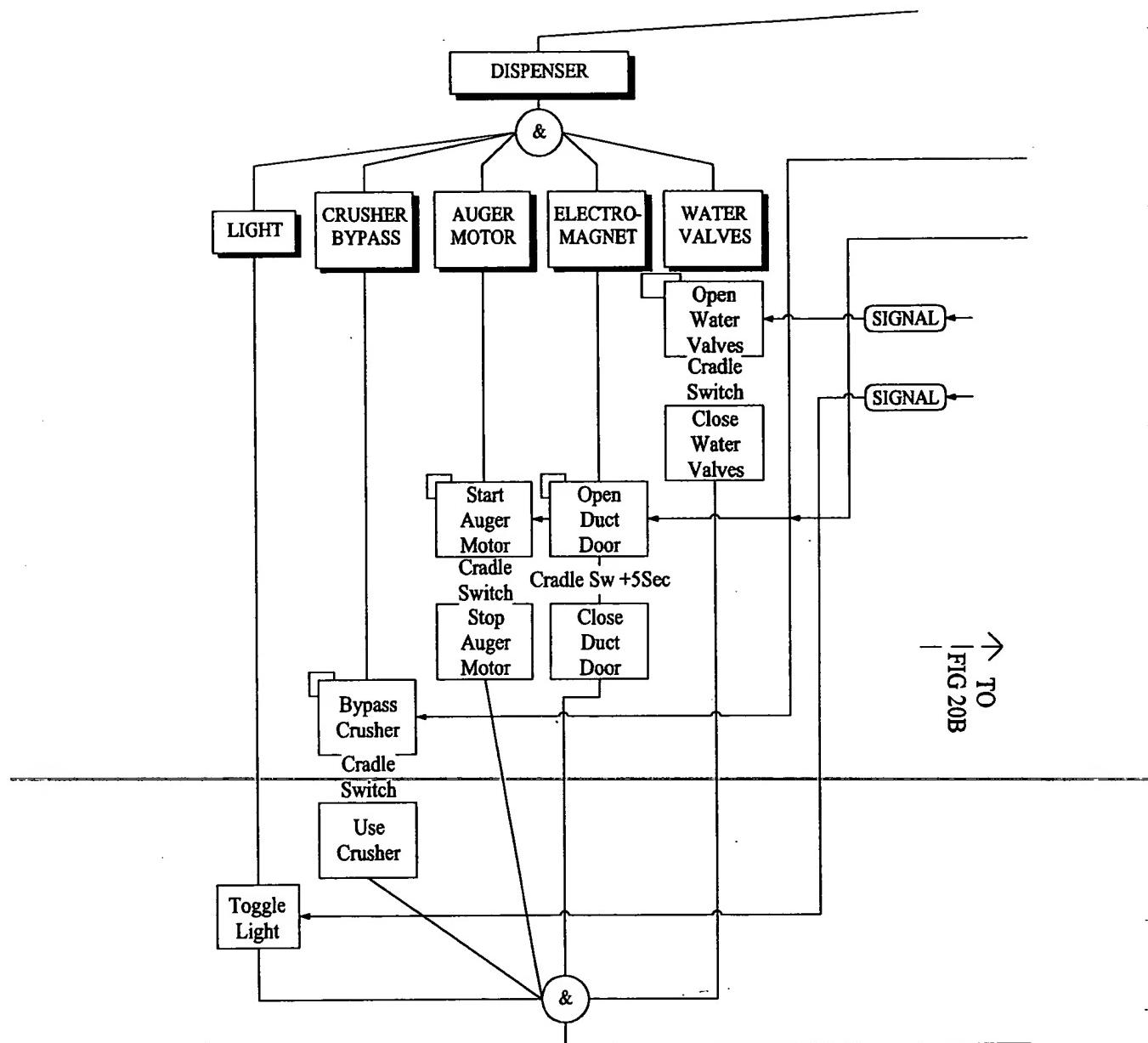


FIG. 20A
Dispenser Behavior

Dispenser Assumptions:

- No Sensor Failures

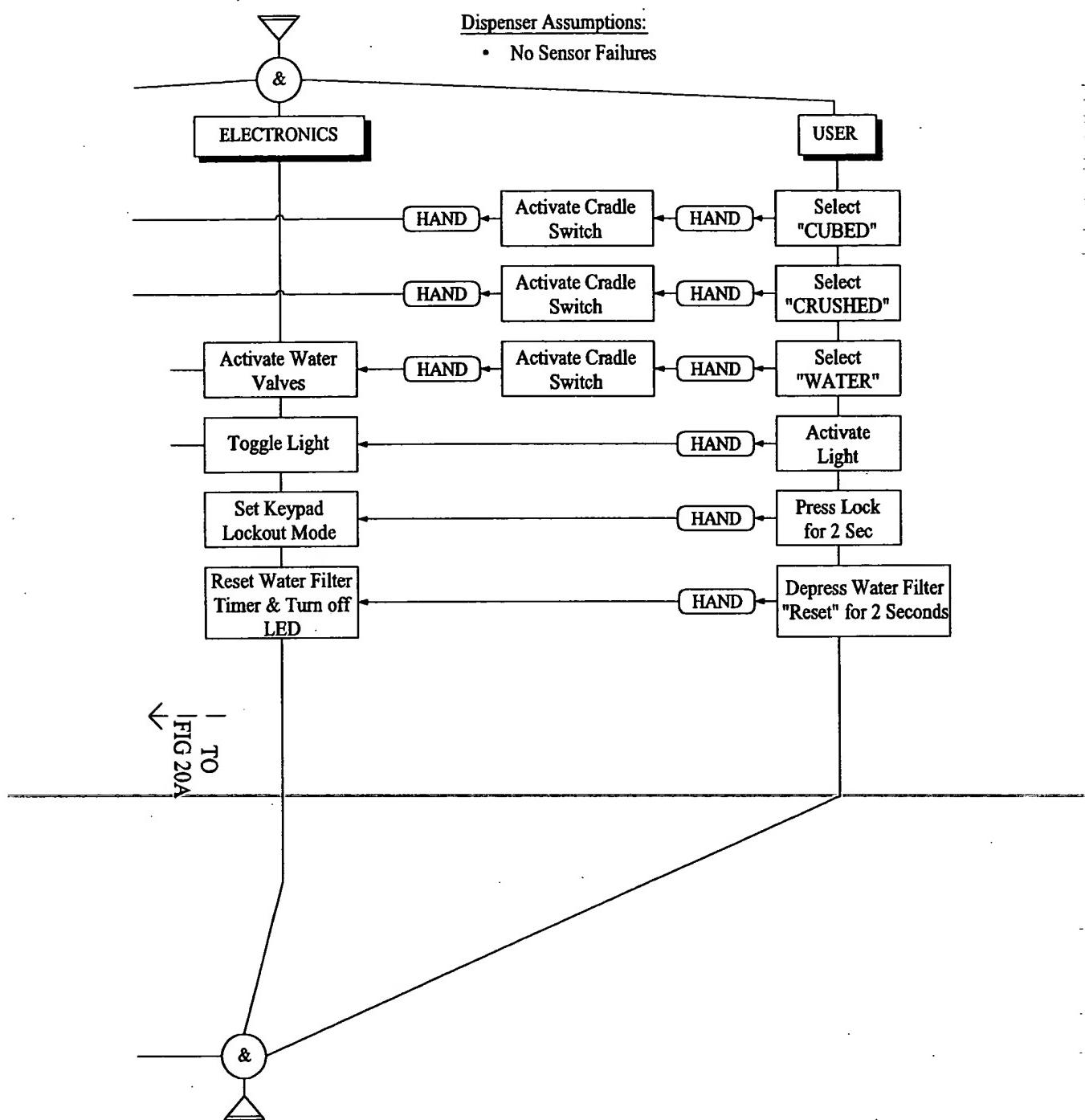


FIG. 20B

Comm Interrupt

526

StateCode
Command
ProcessorTO FIG
41B

Sys Mode

Command
& Rstatus

Parse

Protocol
Data
Parse

Rstatus

Xmit

Command

Protocol
Data Xmt

Char

xPort

Status

Xmit

Status

Char

xPort

Status

Xmit

Status

Char

xPort

Status

Xmit

Port

Char

xPort

Status

Avail (Disable

interrupts)

Physical
Get Rx
Character

Rx Buffer

Note: Any Routine is allowed to read a data store
 The command processor can read or write
 any data store.

Dashed line indicates a path for initialization only

FIG. 41A

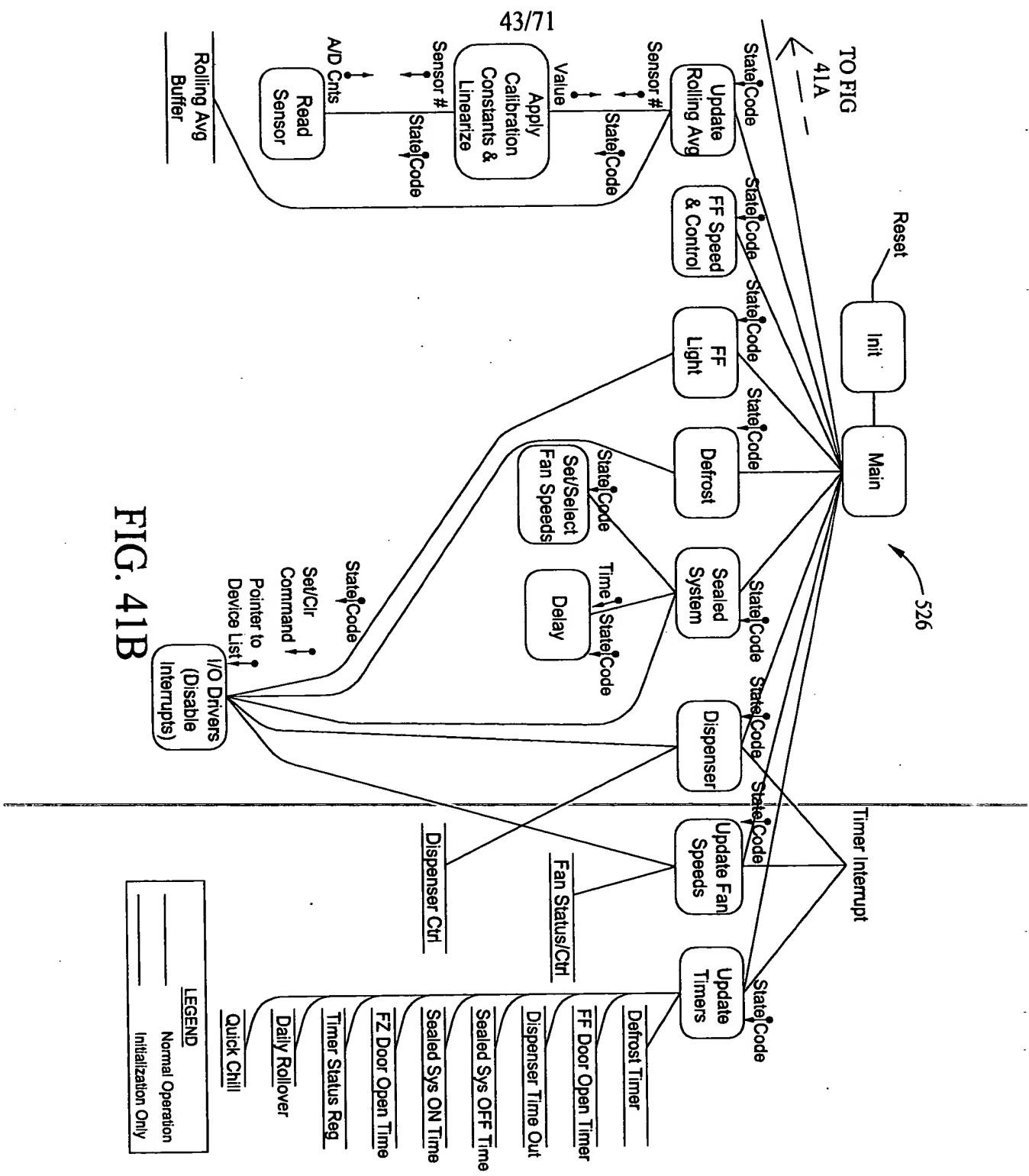


FIG. 41B

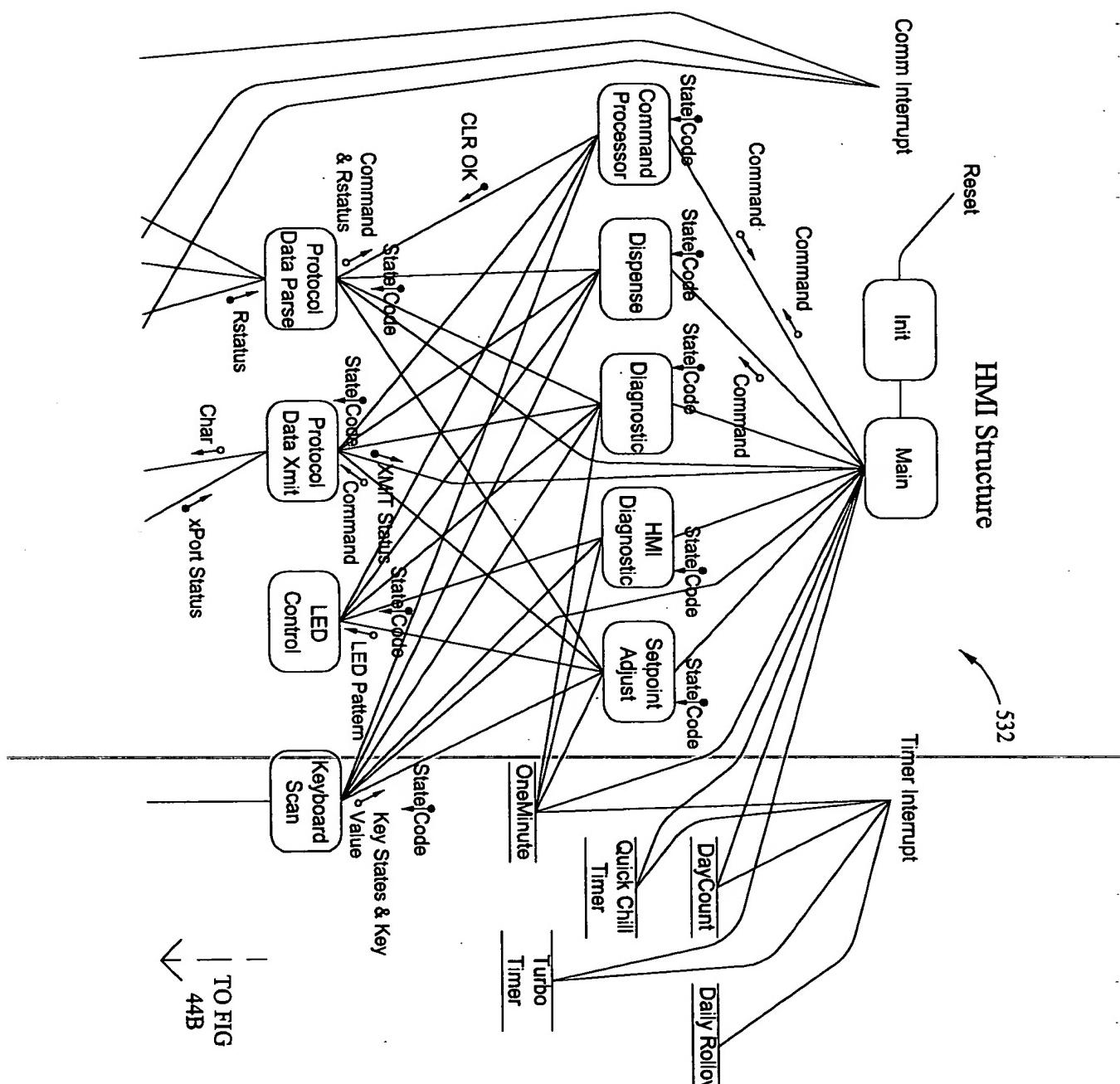


FIG. 44A

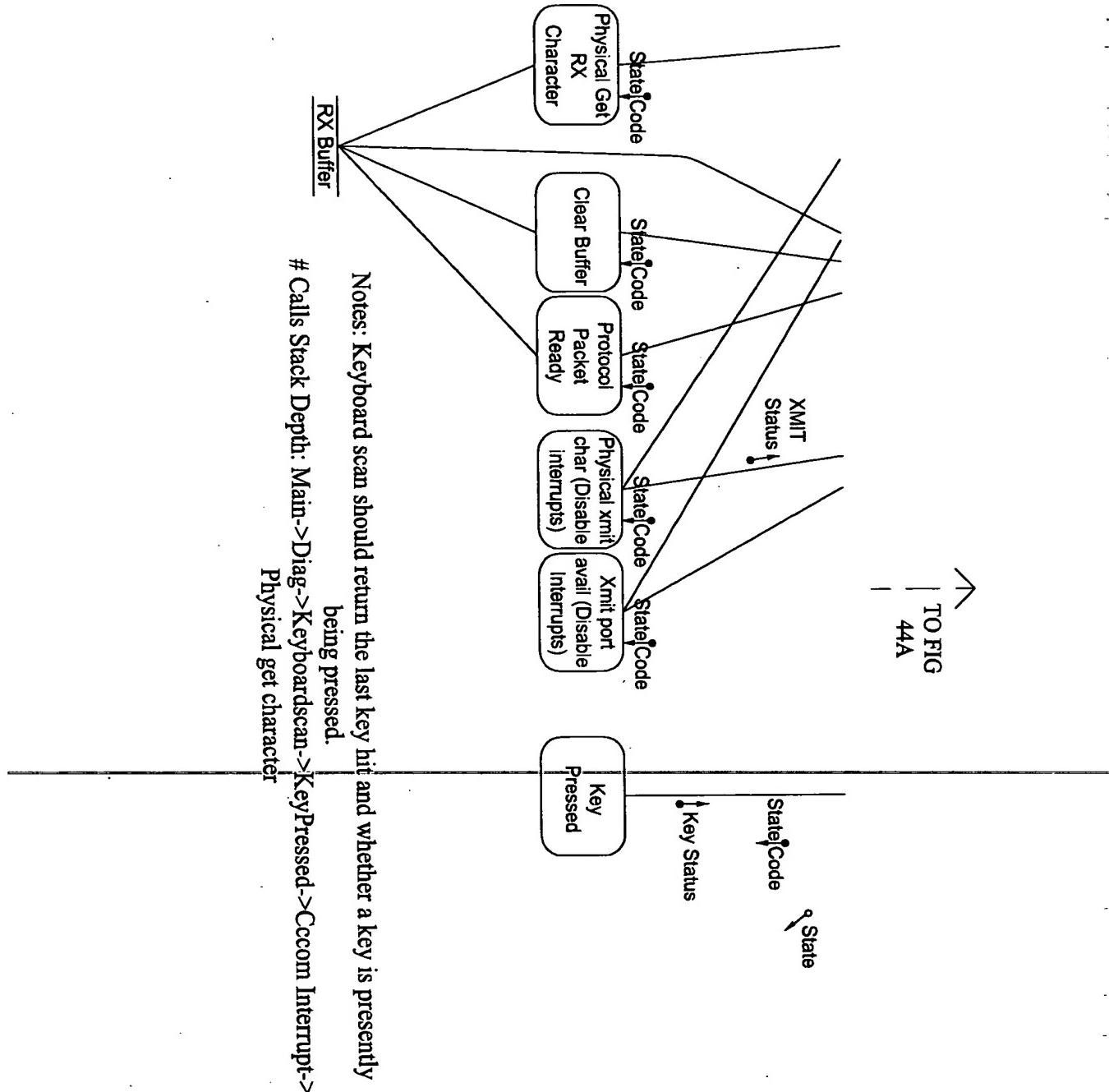


FIG. 44B

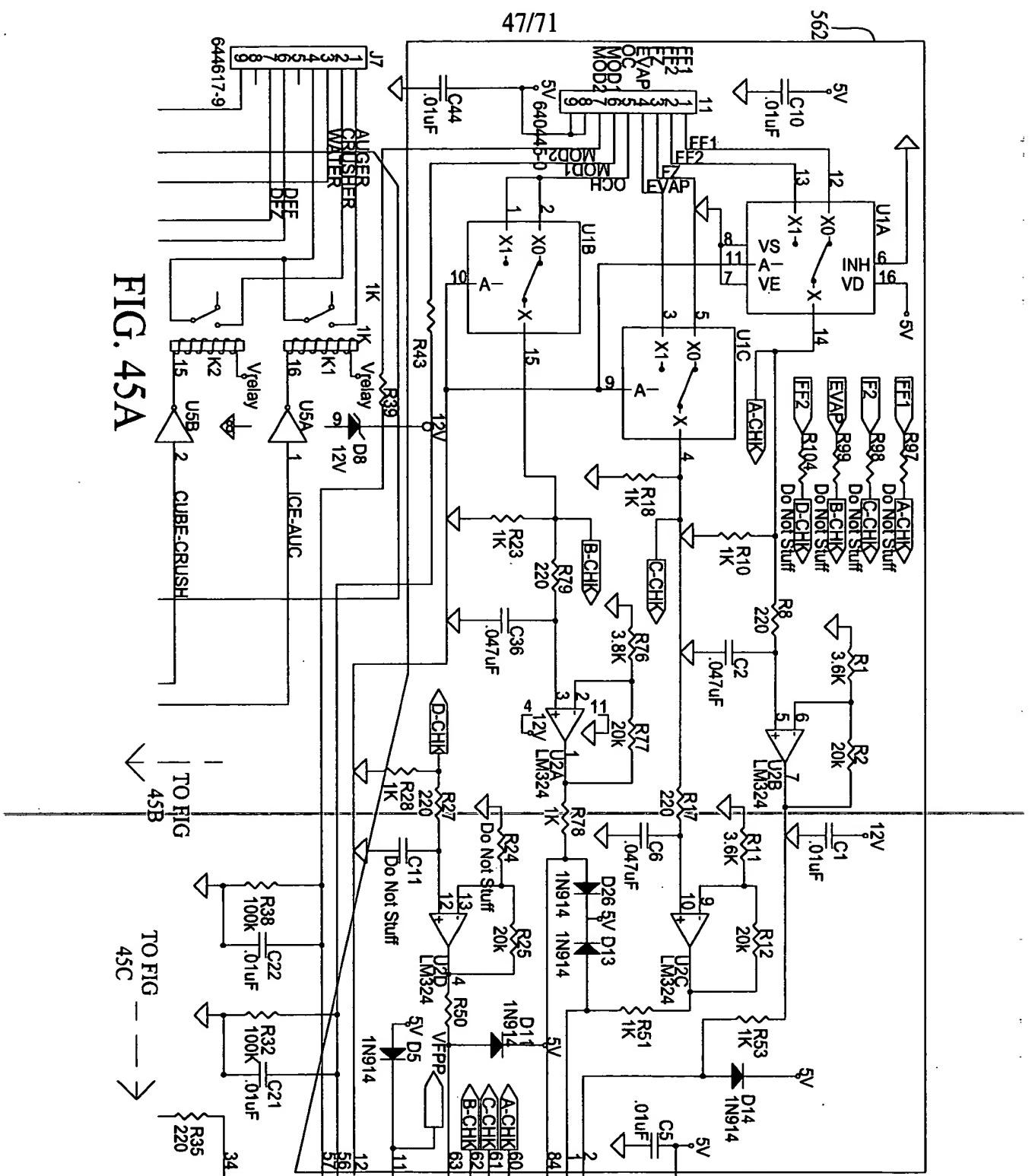
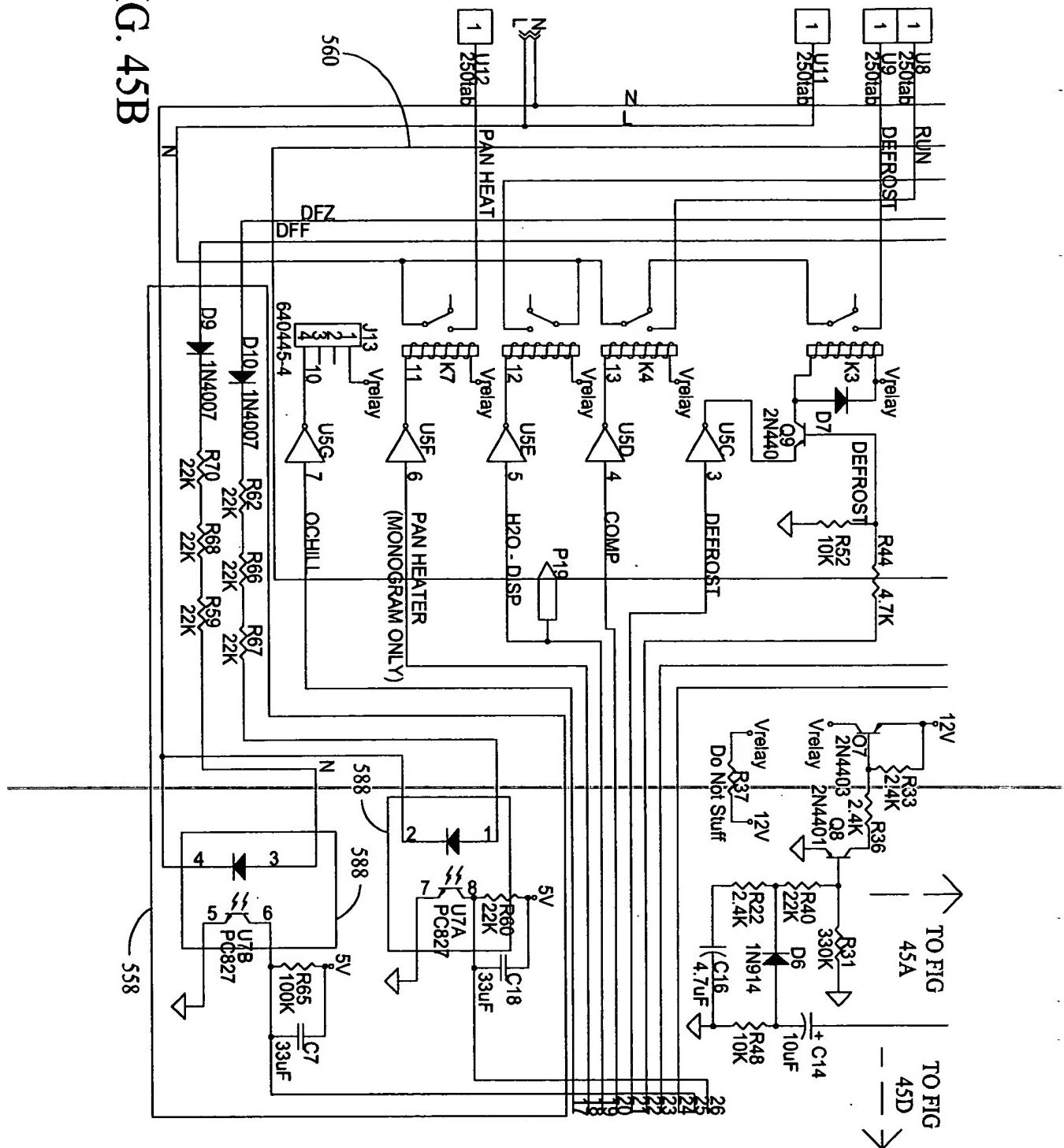
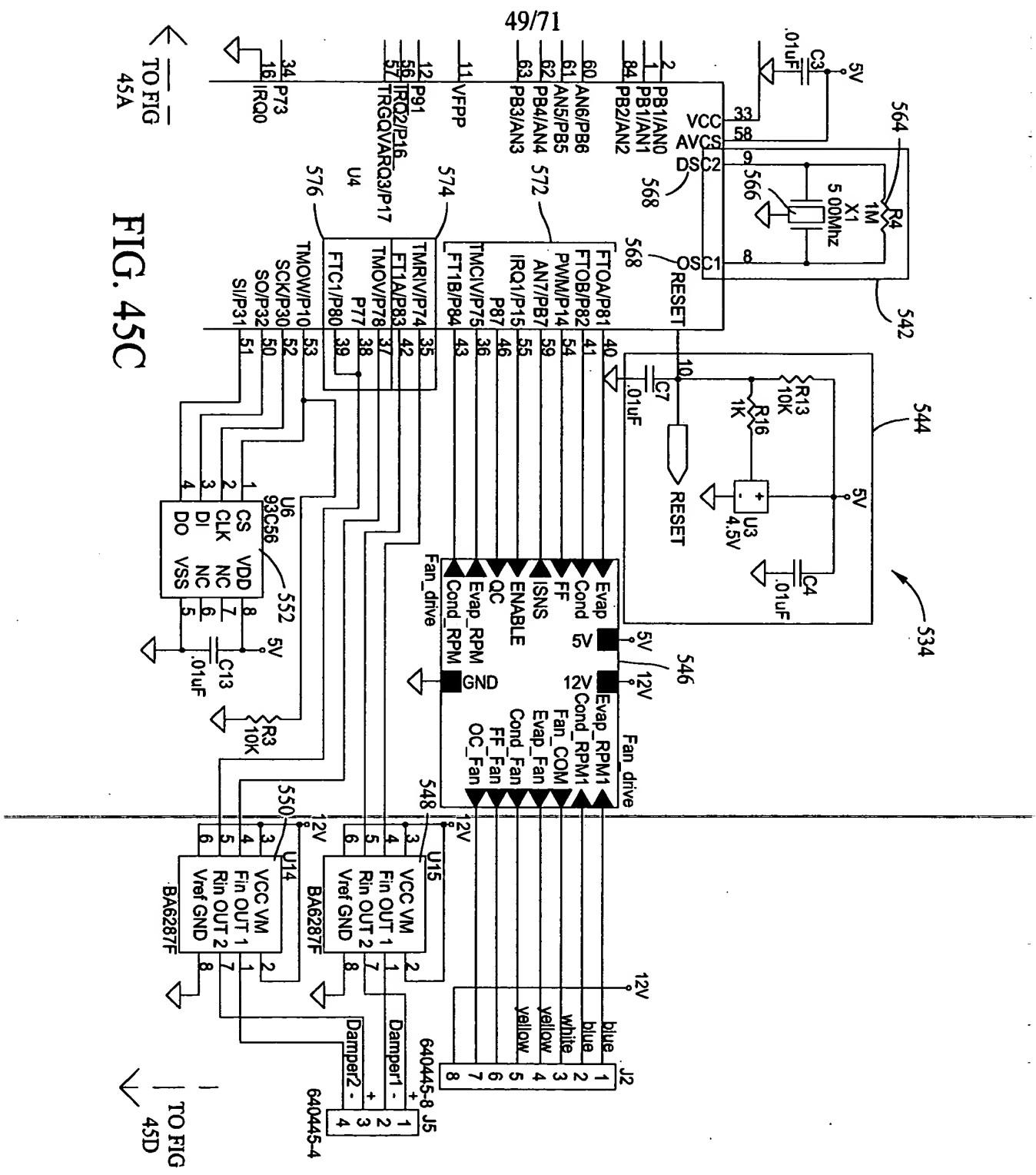


FIG. 45A

48/71





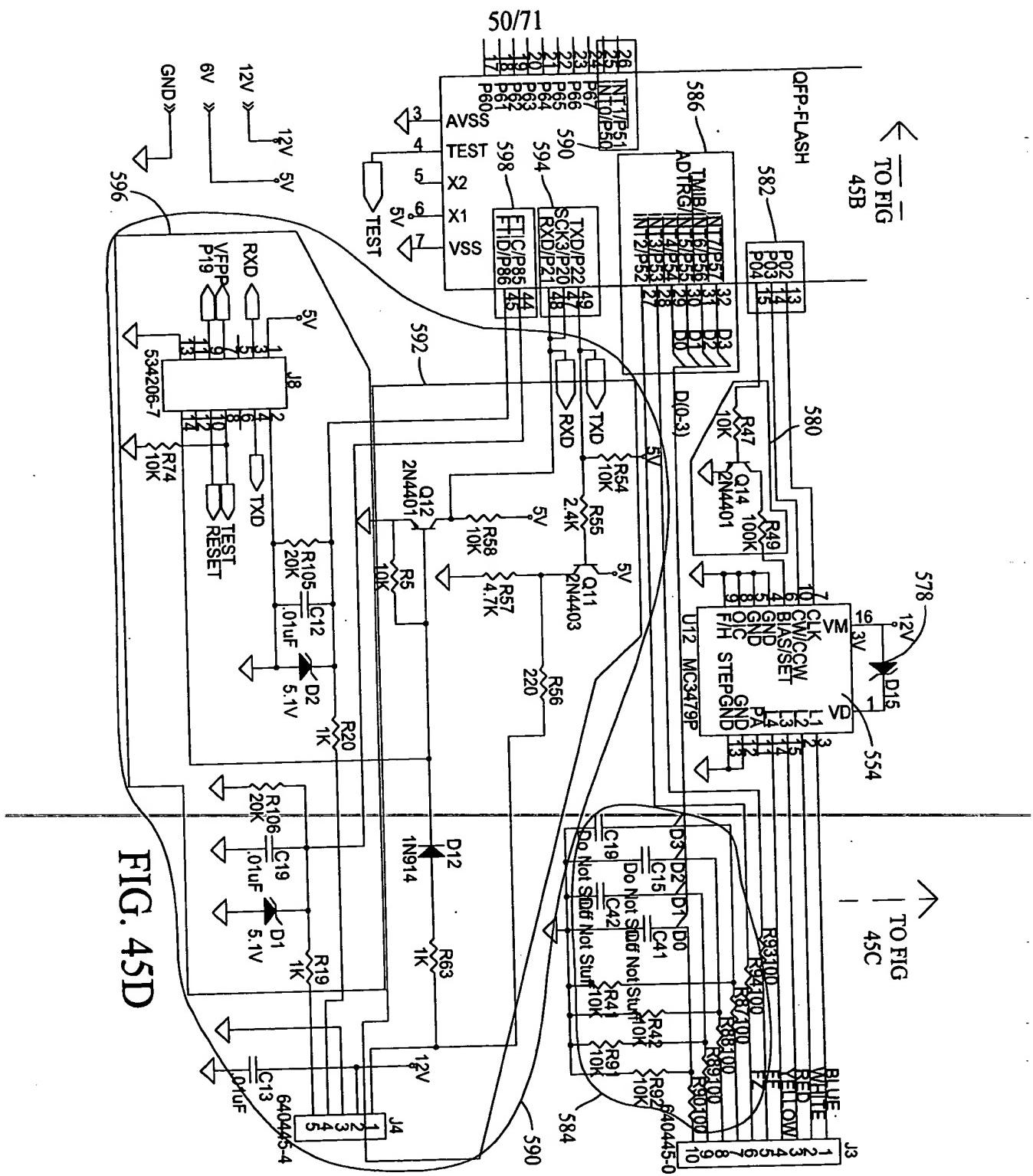


FIG. 45D

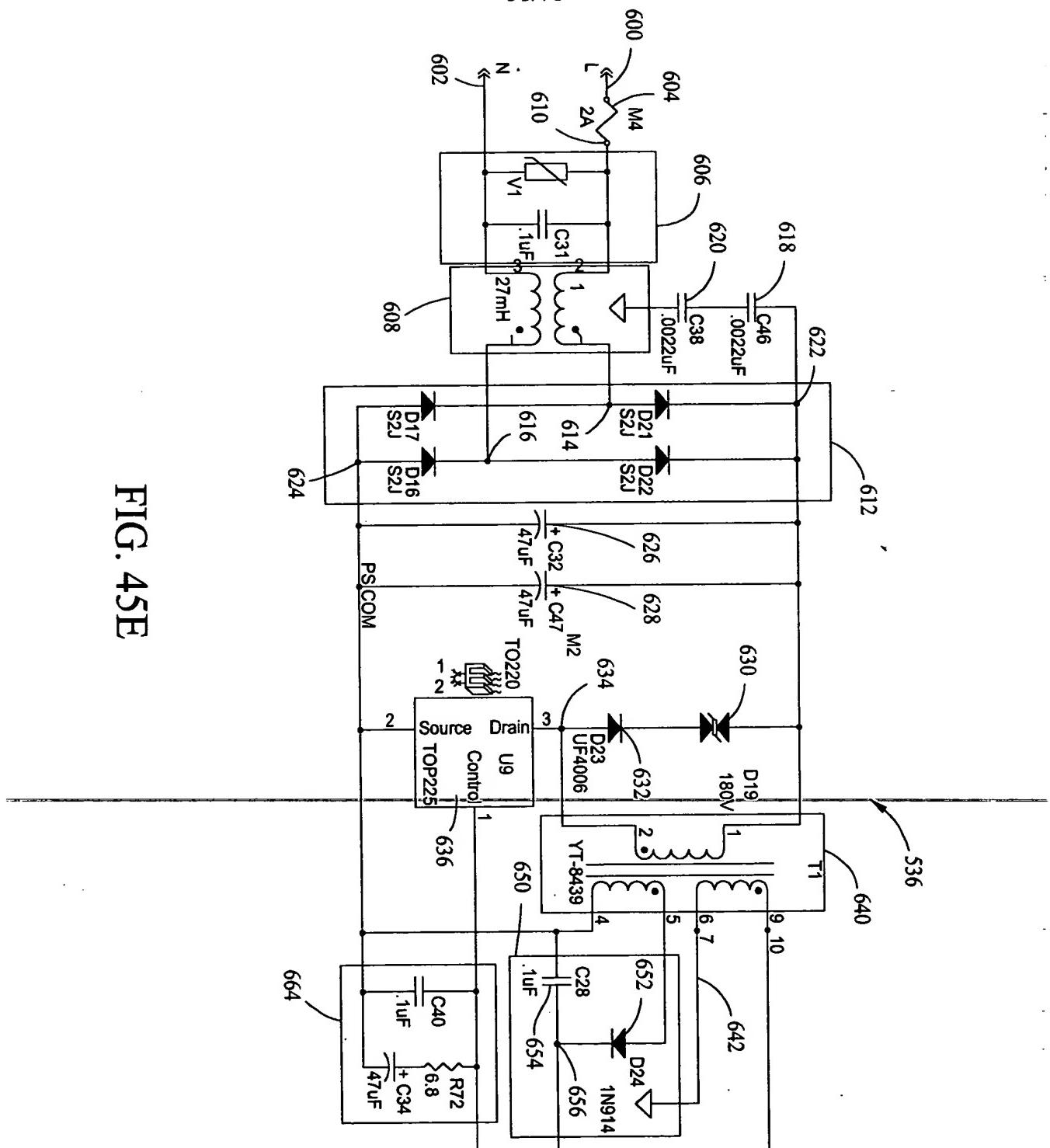


FIG. 45E

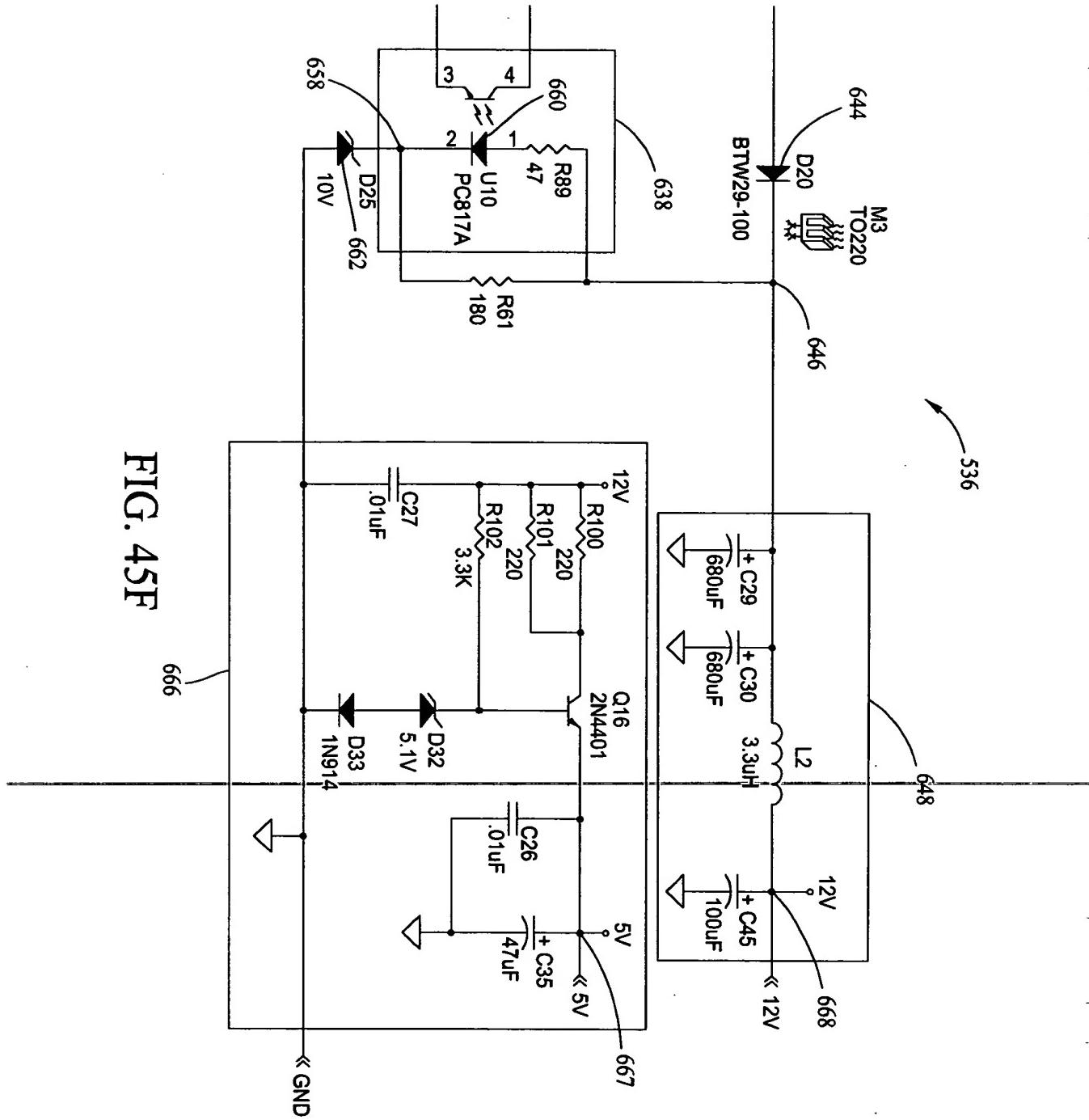


FIG. 45F

53/71

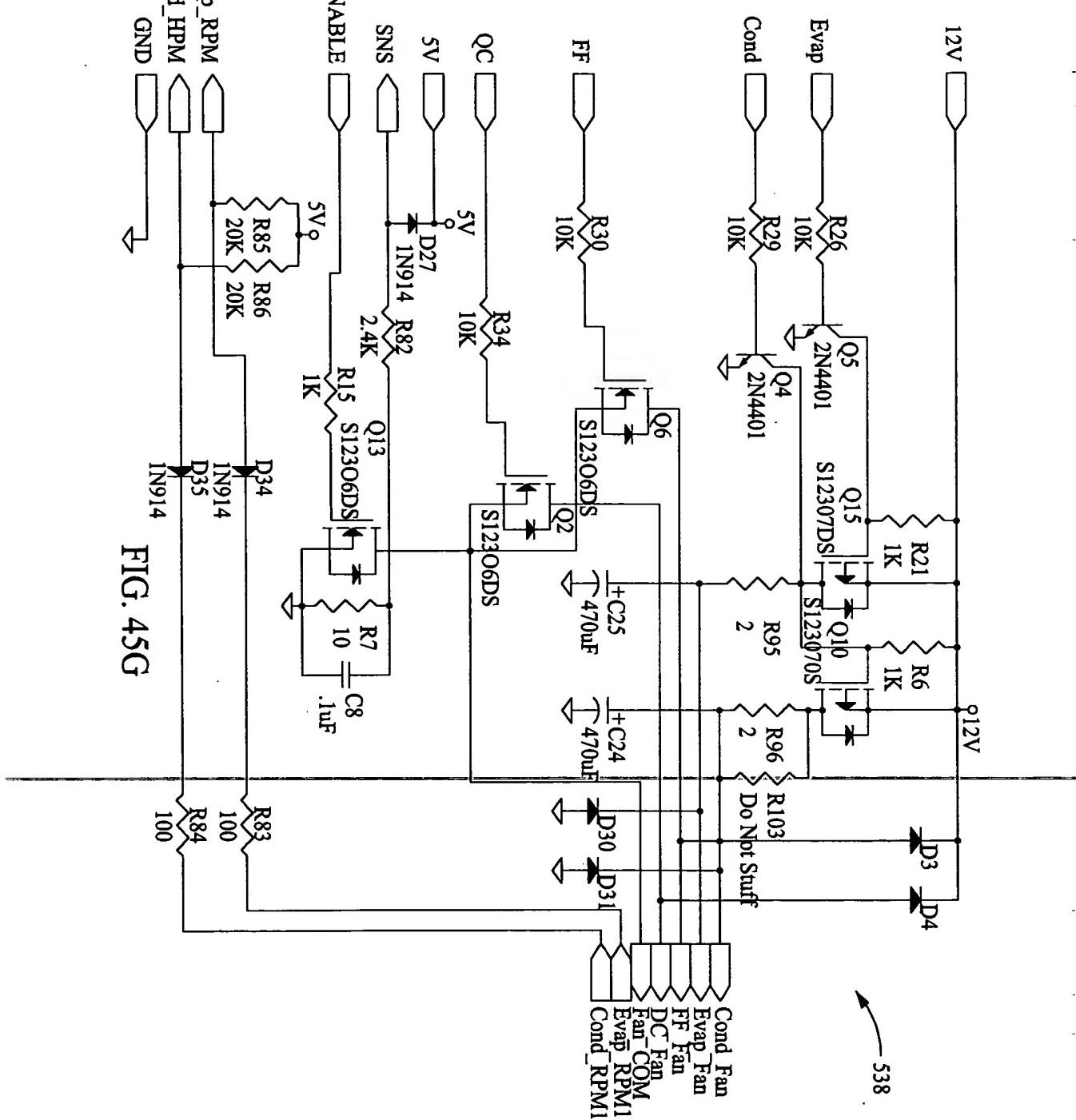


FIG. 45G

538

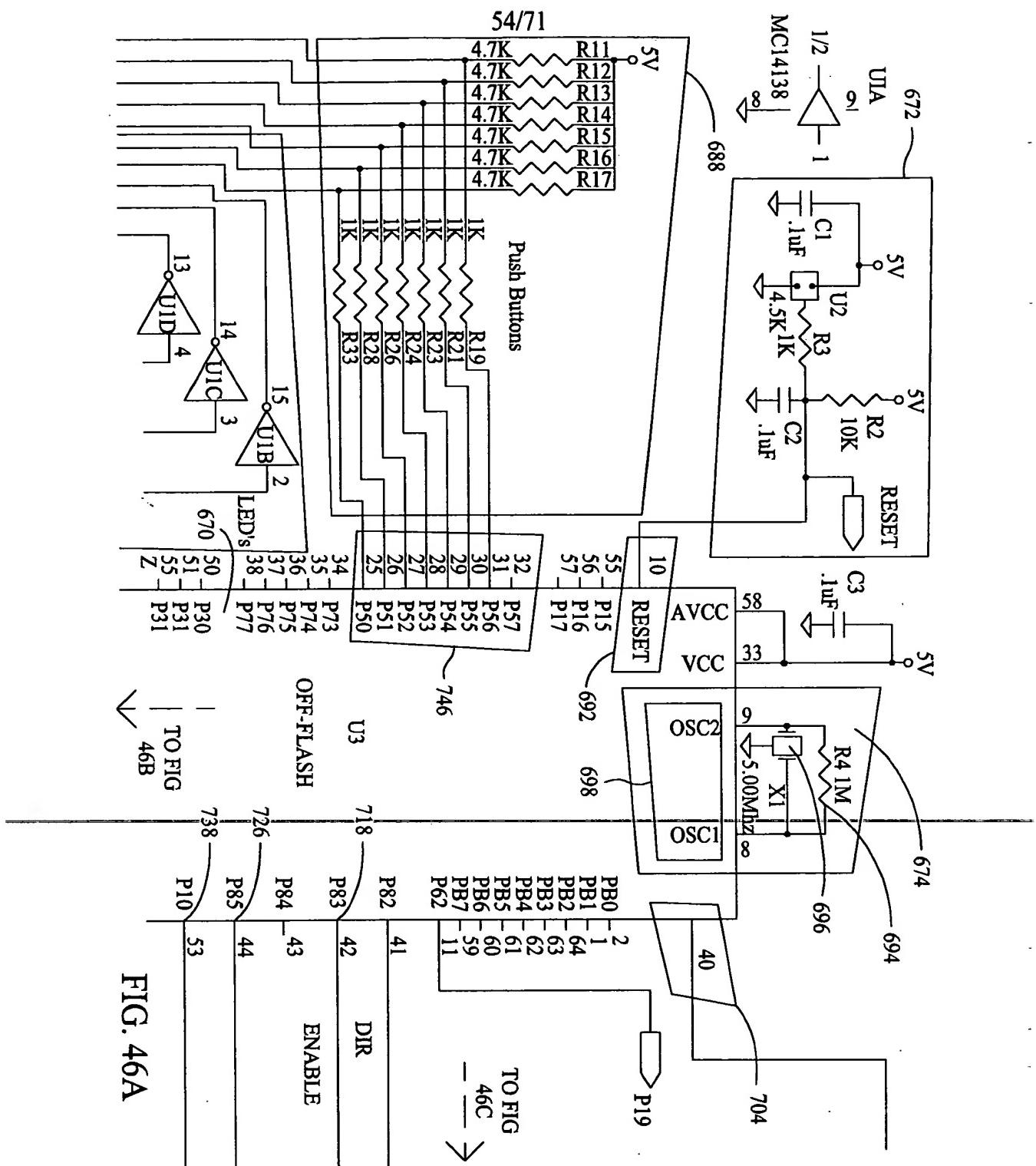


FIG. 46A

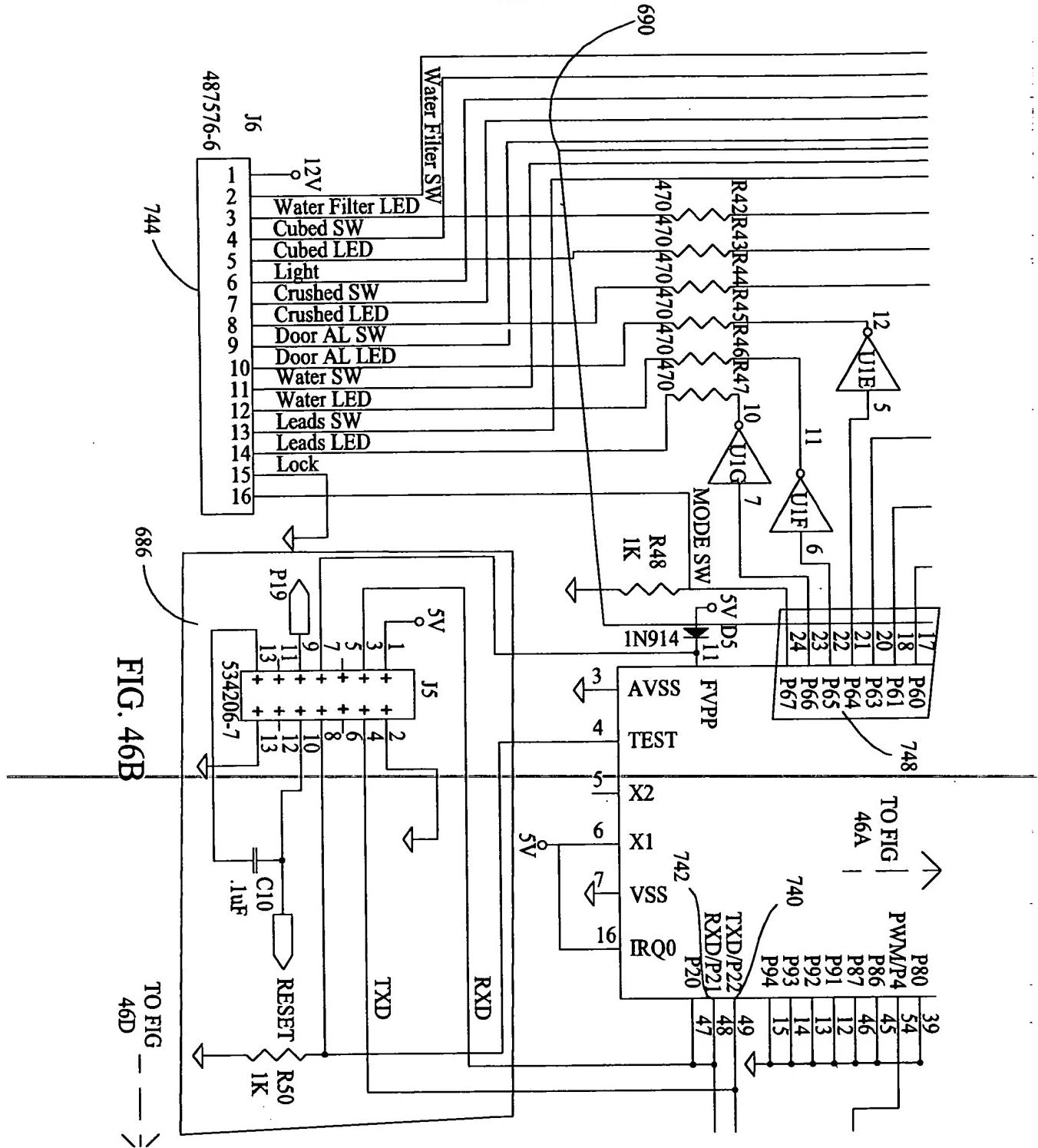


FIG. 46B

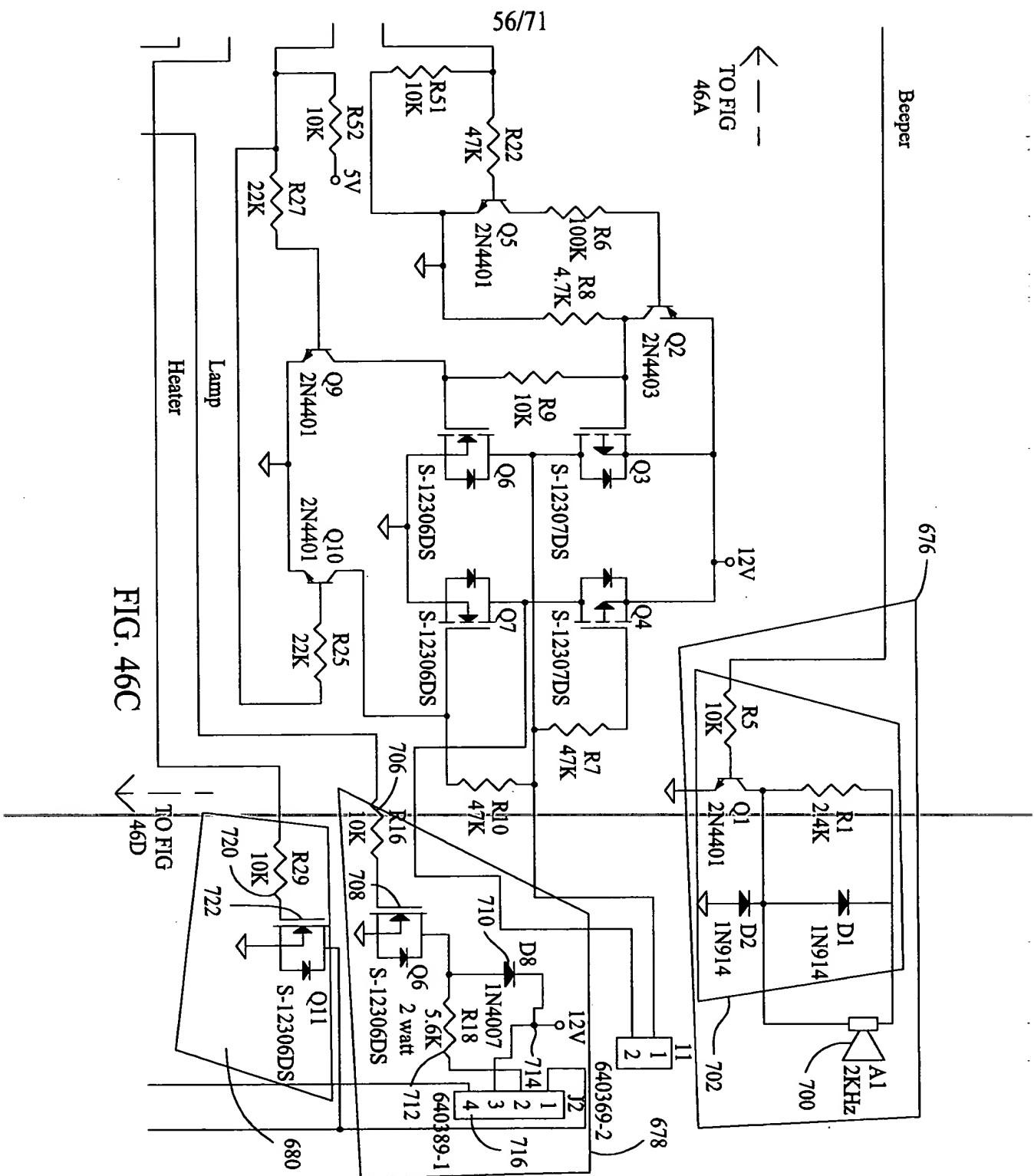


FIG. 46C

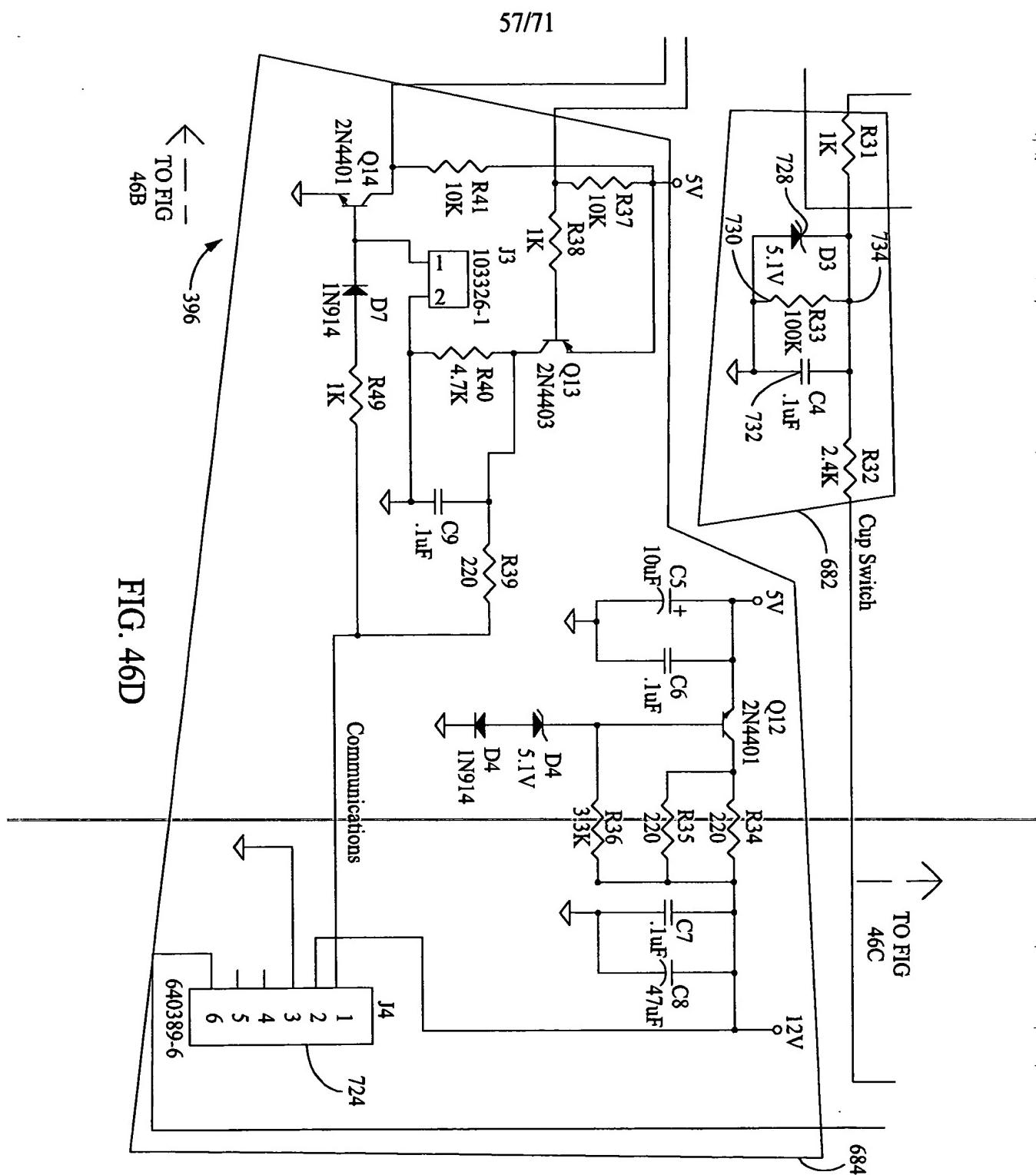


FIG. 46D

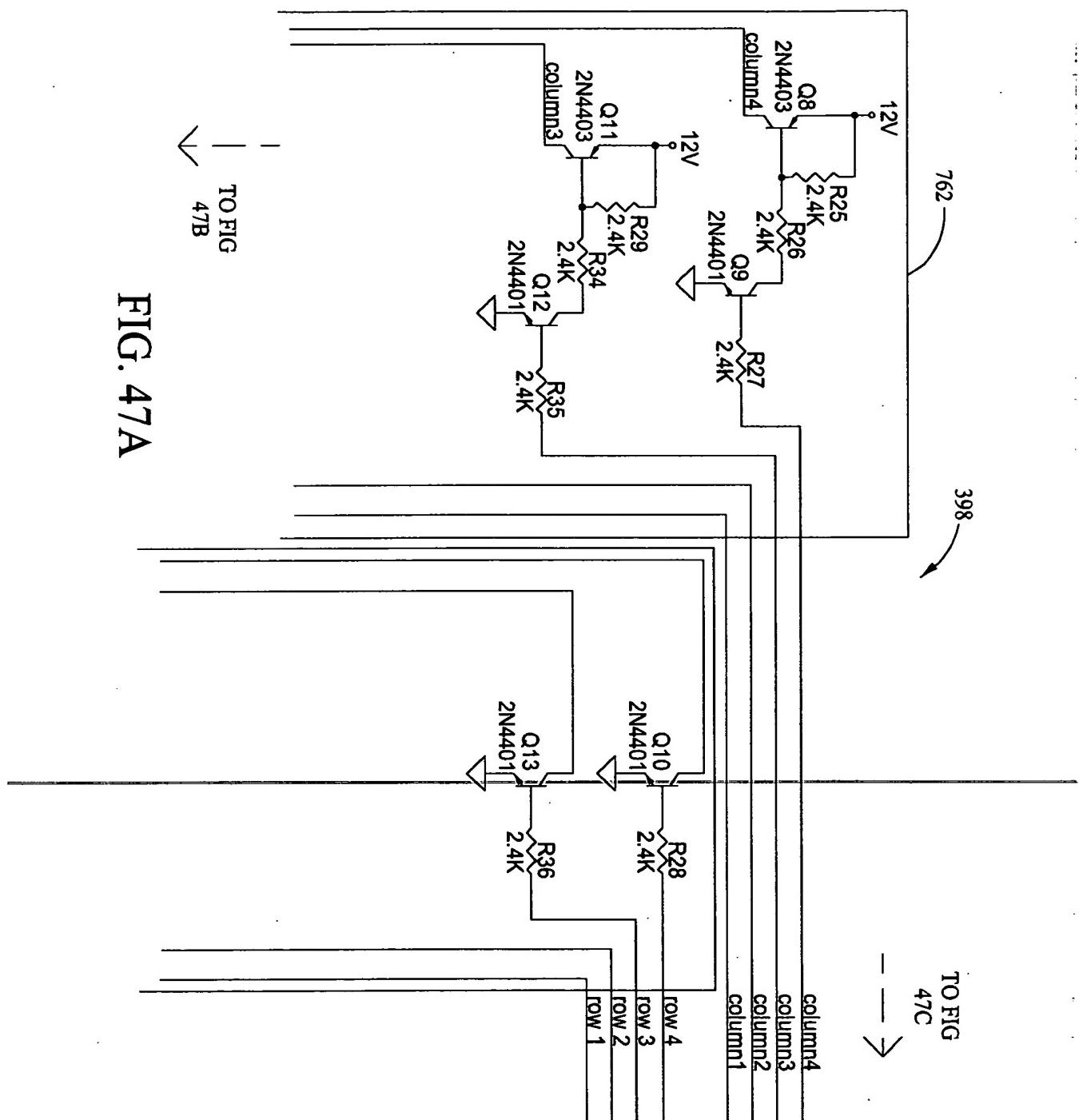


FIG. 47A

59/71

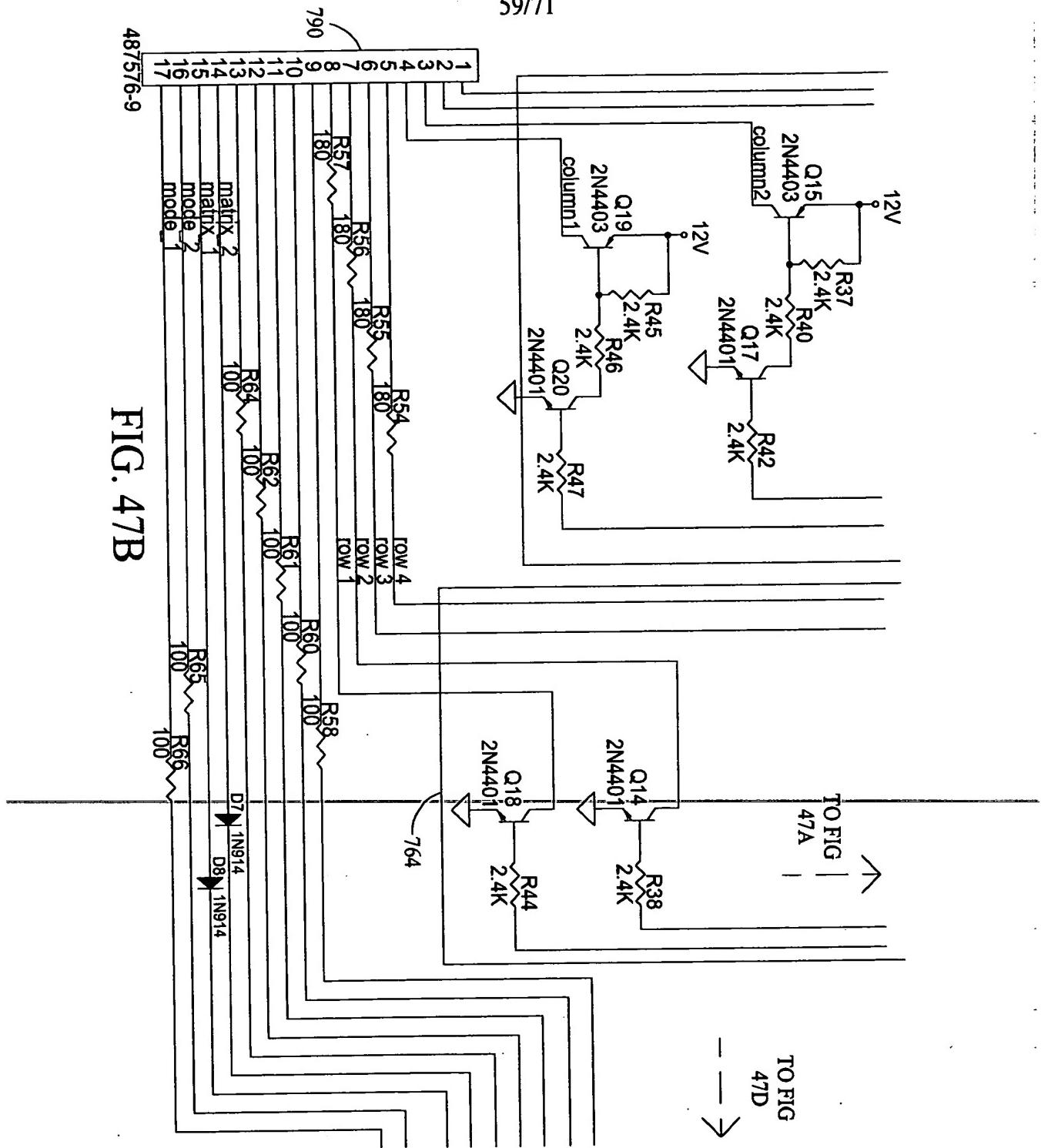
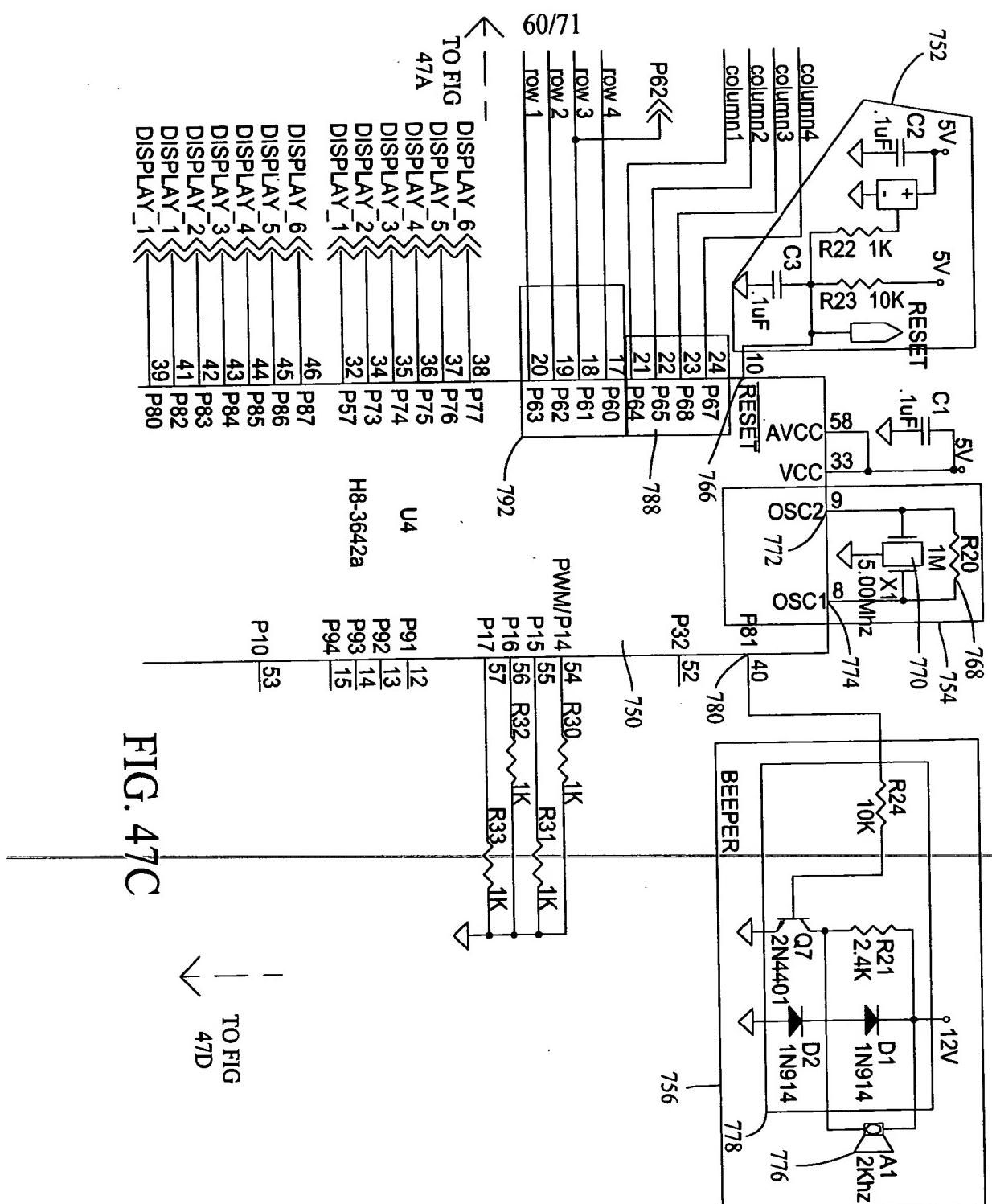


FIG. 47B



61/71

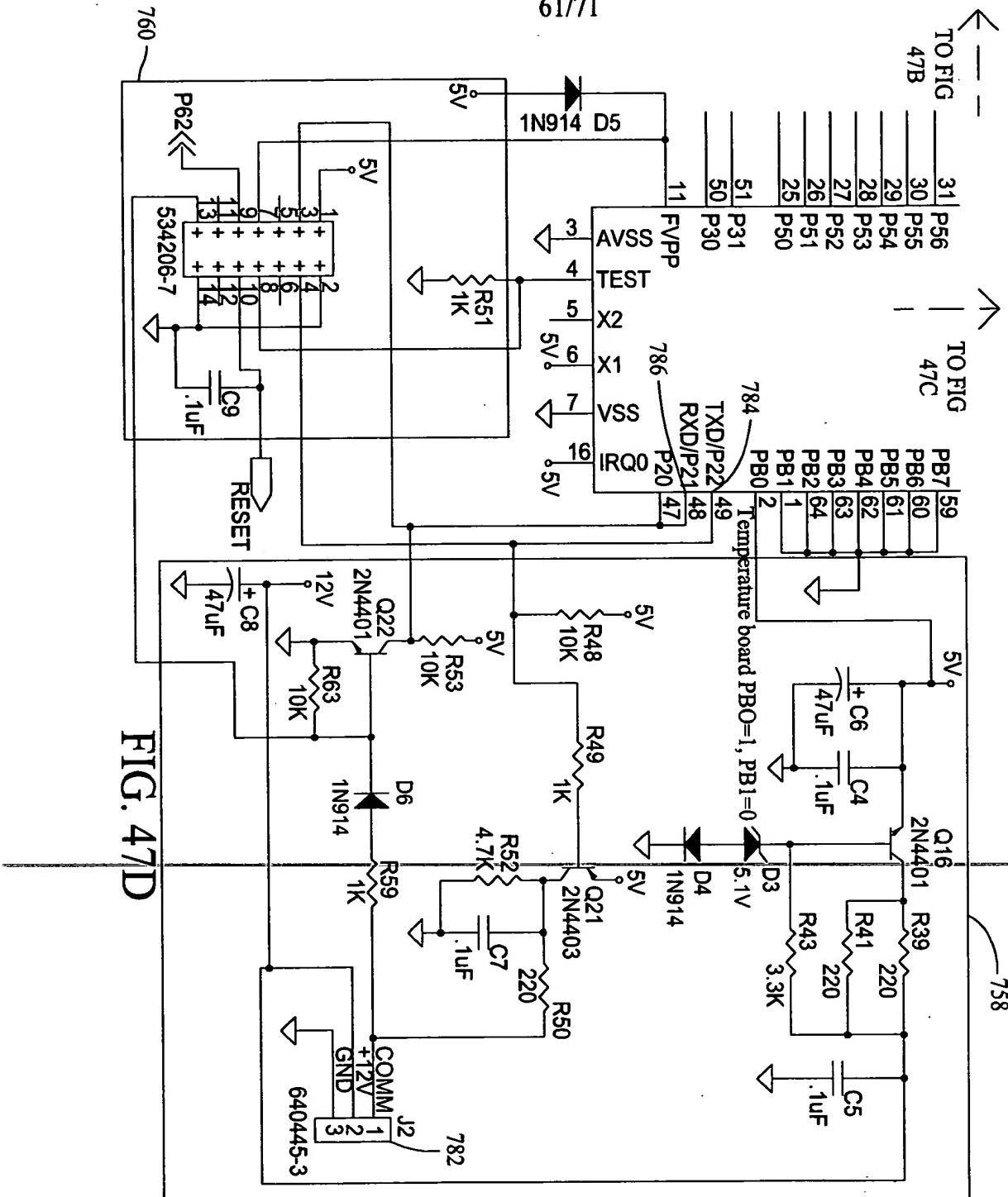


FIG. 47D

760 -